

FIG. 1

FIG. 2 is a block diagram of a system 10. The system 10 includes a central processing unit 12, a memory 14, a hardware random number generator 16a, a pass-thru parallel printer port 18, a pass-thru RS232C port 20, and a processor bus 22. The central processing unit 12 is connected to the memory 14 and the hardware random number generator 16a. The central processing unit 12 is also connected to the pass-thru parallel printer port 18 and the pass-thru RS232C port 20. The memory 14 is connected to the pass-thru parallel printer port 18 and the pass-thru RS232C port 20. The hardware random number generator 16a is connected to the central processing unit 12. The processor bus 22 is connected to the central processing unit 12 and the memory 14. The pass-thru parallel printer port 18 is connected to a printer 24. The pass-thru RS232C port 20 is connected to a host 26 and a target 28.

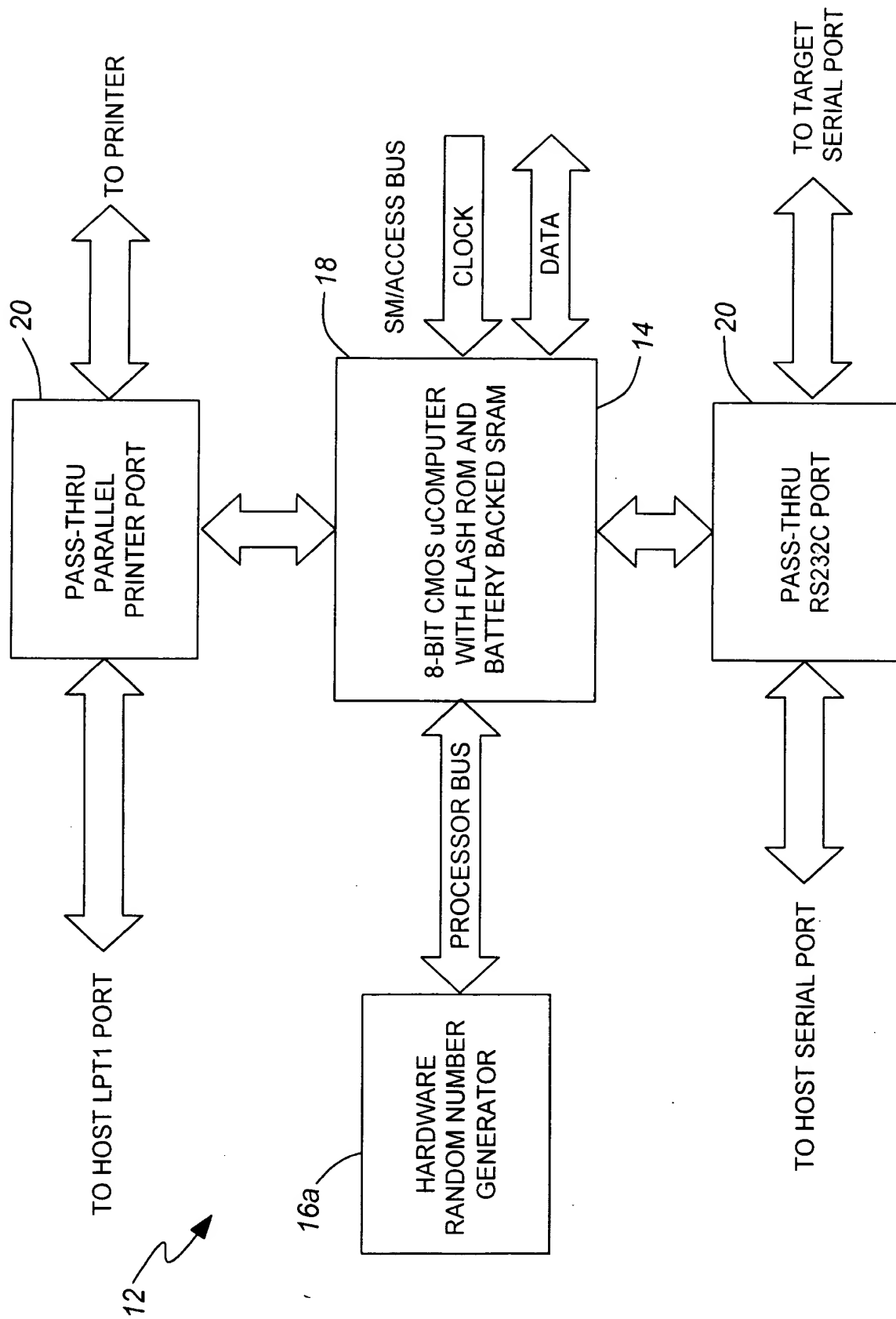


FIG. 2

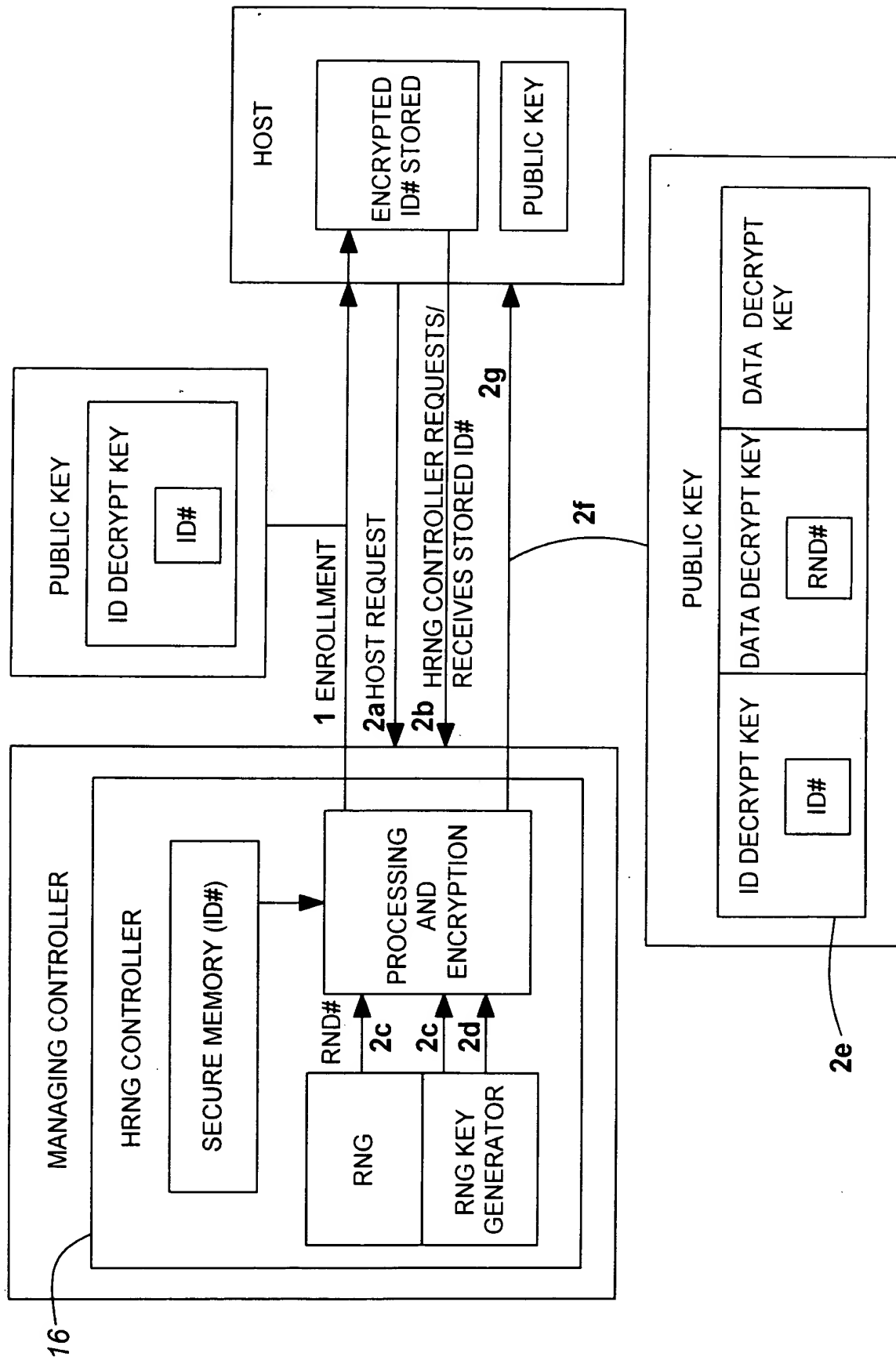


FIG. 3

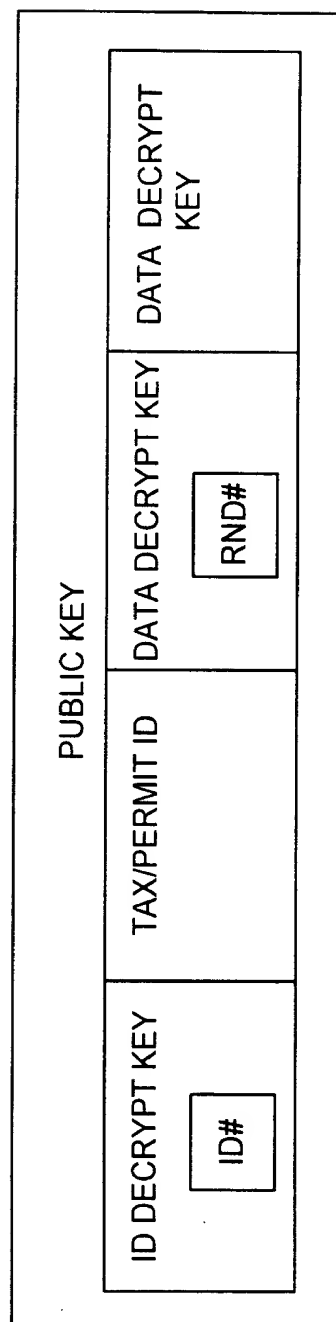
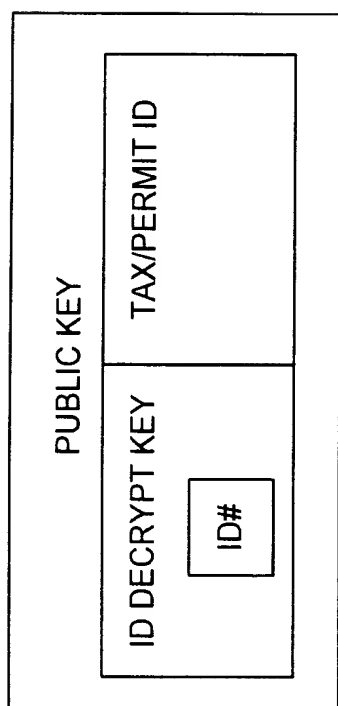
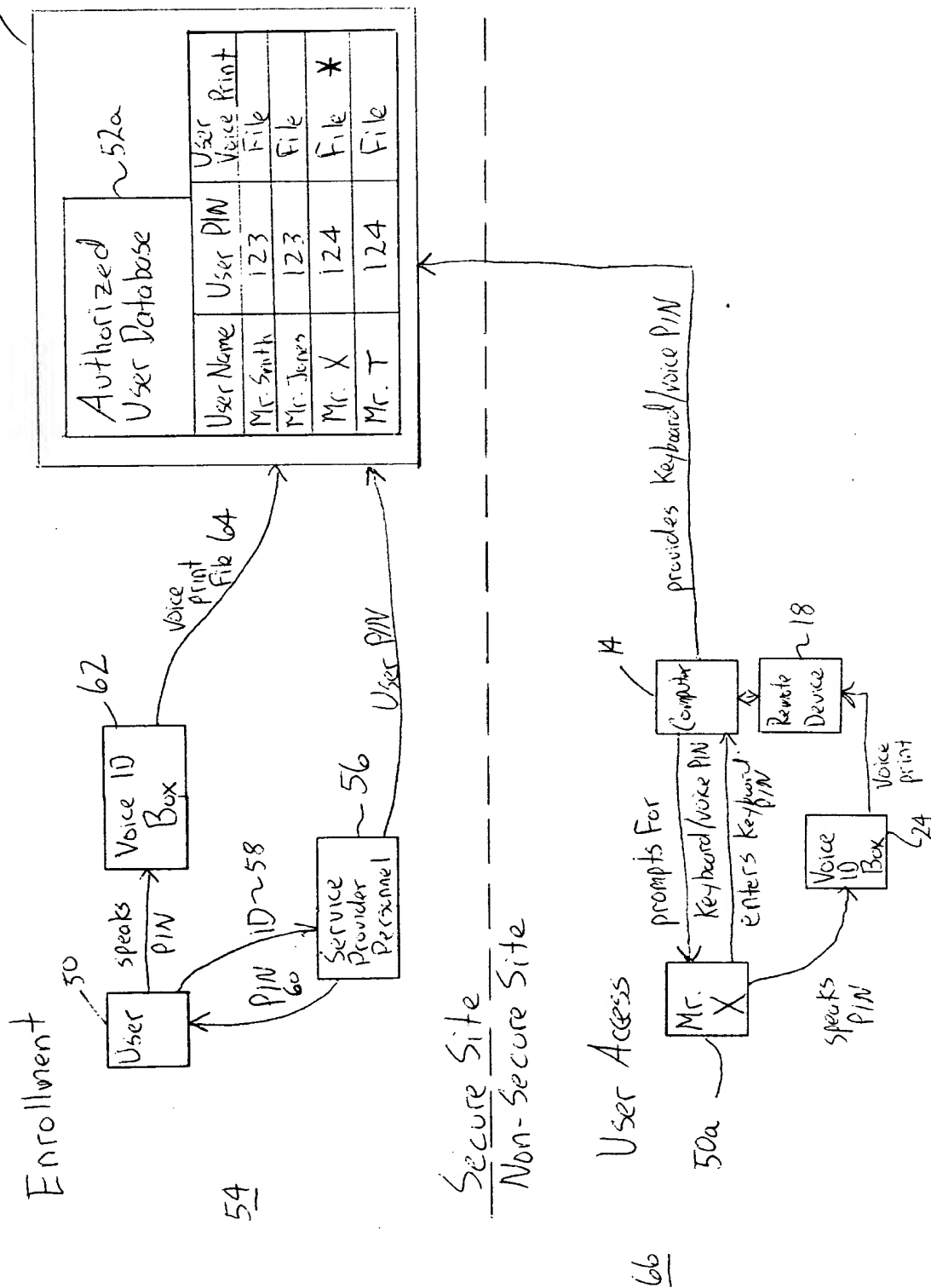


Figure 6



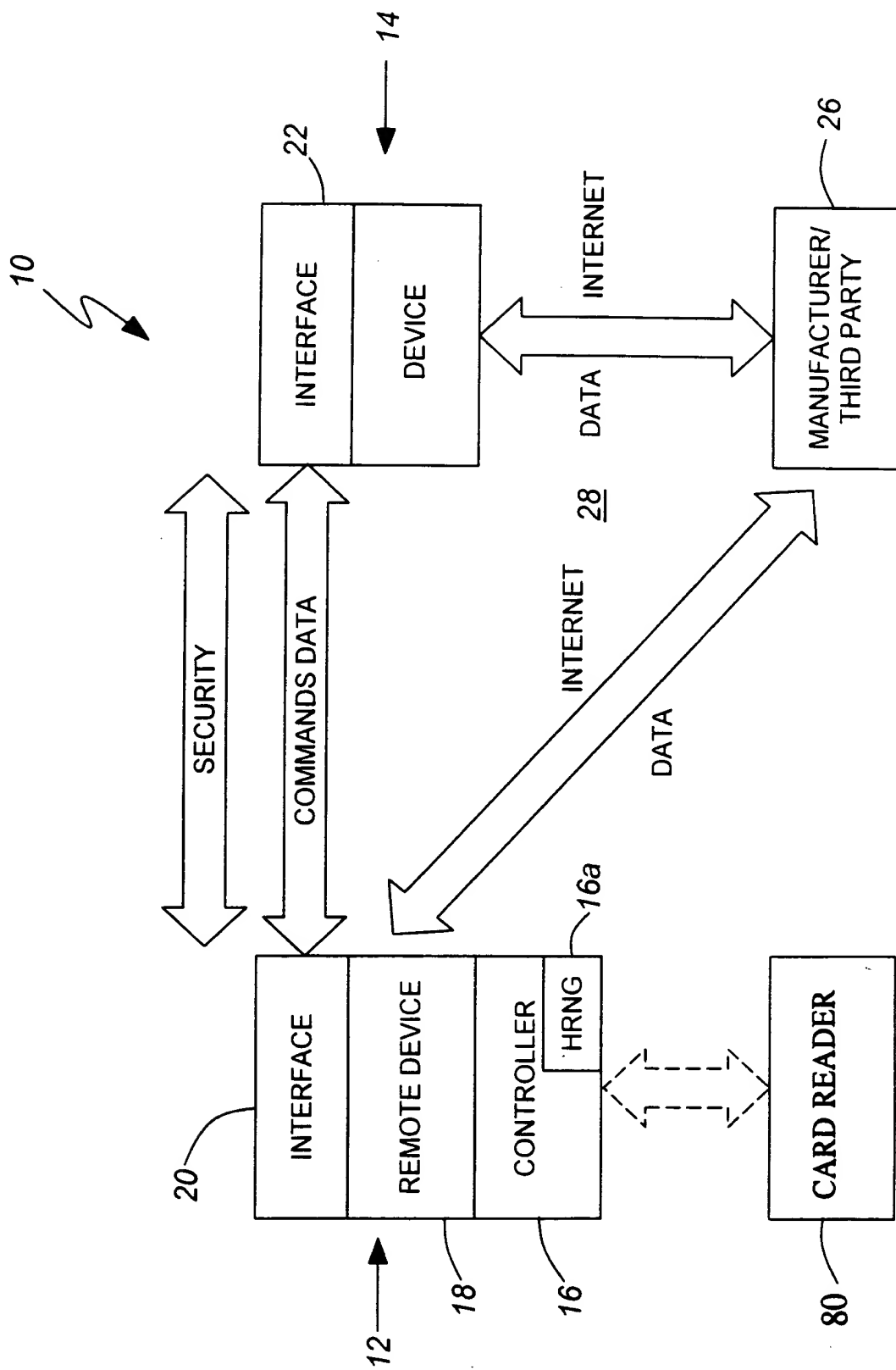


FIG. 7

Figure 8

Device Enrollment & Transaction Authentication

